

	Type	L #	Hits	Search Text	DBs
5	BRS	L5	126573	delay near2 (device\$1 circuit stage\$1)	US-PGPUB ; USPAT ; EPO; JPO; DERWE NT
6	BRS	L6	12807	5 and delay near2 (device\$1 circuit stage\$1) same (resistor\$1 resistance\$1 resistive)	US-PGPUB ; USPAT ; EPO; JPO; DERWE NT
7	BRS	L7	7848	6 and delay near2 (device\$1 circuit stage\$1) same (resistor\$1 resistance\$1 resistive) same (capacitor\$1 capacitance capacitive)	US-PGPUB ; USPAT ; EPO; JPO; DERWE NT
8	BRS	L8	4745	7 and delay near2 (device\$1 circuit stage\$1) with (resistor\$1 resistance\$1 resistive) with (capacitor\$1 capacitance capacitive)	US-PGPUB ; USPAT ; EPO; JPO; DERWE NT

	Type	L #	Hits	Search Text	DBs
9	BRS	L9	1271	8 and delay with (cascad\$3 series)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT
10	BRS	L10	1280	8 and delay\$1 with (cascad\$3 series)	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT
11	BRS	L11	841	10 and delay\$1 with voltage\$1	US- PGPUB ; USPAT ; EPO; JPO; DERWE NT